



Agenda

- Part I: 9:00 am - 10:45 am

- 9:00 am Status Report and charge: (J. Incandela/FNAL)
 - Summary of the first workshop and an overview of what we've learned and done since then.
 - Physics goals revisited
 - Response to PAC (**)
- 9:30 am Tracking with the Run 2a detector: (R. Snider/FNAL)
 - Implications for Run 2b
- 10:00 am Lifetimes revisited: (S. Worm/New Mexico)
 - Central expectations and reasonable worst case estimates for Silicon+SVX3D chips by layer.
- 10:30 Portcards (S. Zimmerman)
- 10:40 DOIMS (P. Chang)
- 10:50 SVX3 (V. Nagaslaev)

- Part II: 11:00 am - 3:00 pm

- 11:00 am SVX4 simulation studies: (Aaron Dominguez/LBNL)
- 11:30 am Material and cost considerations for hybrids: (CarlHaber/LBNL)
- 12:00 pm Discussion of PAC Request
- Lunch 12:30-1:30
- 1:30 pm - 2:30 pm Pixels (W. Wester/FNAL)
- 2:30 pm - Discussion of next steps

- Part III 3:00 pm - 5:30 pm

- Working group meetings



PAC Question

- What are the relative merits and implications of several strategies:
 - Replace only Layer 00
 - Replace Layer 00 and only the inner 1 or 2 layers of SVXII
 - Complete rebuild of Layer 00 and SVXII
- Need detailed response by Fall.
 - We should aim for a proposal with cost and schedule.